DIGITAL DESIGN PRINCIPLES AND PRACTICES, Second Edition
by John F. Wakerly
Errata as of 2/18/97

TECHNICAL ERRORS/PROBLEMS

Page	Location	Description
87	Figure 3-18(c)	omit inversion bubble on logic symbol for CMOS noninverting buffer
94	Table 3-3	"tpLH, tpHL" (third set) --> "tpZH, tpZL" "tpLH, tpHL" (fourth set) --> "tpHZ, tpLZ"
129	Lines 8-9	"below ... AC)." --> "below 2.4 V, which is VIHmin for typical driven gates plus a 400-mV noise margin."
130	Figure 3-56	NAND inputs should all be HIGH-HIGH. "100 uA" --> "60 uA", "40 uA" --> "20 uA" (twice)
	Line 2	"LS-TTL" --> "typical LS-TTL", "40" --> "20"
	Line 4	"(4*5) + (2*40) = 100 uA" --> "(4*5) + (2*20) = 60 uA"
	Lines 9, 10	"26" --> "43.3"
131	Figure 3-58	This multiplexer circuit produces an inverted output
158	Figure 3-81	topmost D1YB --> D1XB
159	Line 3	"Q3A" --> "Q5A"
	Lines 5-6	"the emitter of Q1B" --> "diode D1XB"
	Line 9	"required by Q1B and reversed-biased protection diode" --> "flowing through reverse-biased diodes D1XB and D2XB"
346	Fig. 5-74	"XGTY4"<->"XLTY4", "XGTY8"<->"XLTY8", "XGTY"<->"XLTY"
367	Line 11	"16" --> "22"
	Line 12	"16" --> "17"
	Line 13	"21" --> "24"
	Line 20	"37" --> "41"
	Line 22	"56" --> "63"
	Line 25	"153" --> "164"
432	Line -2	"the /EN input as a three-state control" --> "the /G1 and /G2 inputs as three-state controls"
577	Drill 8.5	Delete "but one", "or incorrect or both", "and errors"
676	Table 10-6	Change set definitions for LEFTIN and RIGHTIN: LEFTIN = [QB, QC, QD, LIN]; RIGHTIN = [RIN, QA, QB, QC];
677	Table 10-7	Same change as in Table 10-6
711	Line -3	"four" -- > "five"

TYPOGRAPHICAL/PRODUCTION ERRORS AND CLARIFICATIONS

Page	Location	Description
xvii	Lines 9,12	"Chapter 2" --> "Chapter 3"
xviii	Line -3	"1.14" --> "2.14", "1.15" --> "2.15"
xviii	Line -2	"1.16.1" --> "2.16.1"
xviii	Line -1	"Chapter 6" --> "Chapter 8"
xxi	Line -4	invert first set of quotation marks for "can-do"
xxii	Line 25	"parts the text." --> "parts of the text."
3	Line -10	"successful practioner" --> "successful practitioner (but lousy speller)"
5	Box line -1	delete "a"
	Line -7	"a element" --> "an element"
6	Line -2	delete extra "the"
8	Line 8	delete extra "an"
9	Line 1	"agressive" -- > "aggressive"
11	Line 9	"able produce" --> "able to produce"
11	Line -9	delete "em" and italicize "bonding pads"
11	Line -1	delete extra "the"
13	Line 6	"the pinout" --> "the same pinout"
18	Box line 6	"a circuitry" --> "of circuitry"
55	Line 15	"110 and 011" --> "110, 011 and 101"
55	Line 16	"110 or 011" --> "110, 011 or 101"
65	Line -10	"Figure 2-15" --> "Figure 2-16"
67	Box line -2	"preesented" --> "presented"
70	Line -7	add parentheses: "[x+y] = ([x] + [y]) modulo 2^n"
76	Line 8	"only possibility" --> "only one possibility"
86	Line -1	Add sentence: "Combining Figure 3-15(a) with an inverter yields an OR gate."
93	Note 5, line 3	"suppy" --> "supply"
110	Line -3	"for logic designer" --> "for a logic designer"

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118  Line 21          "amount power" --> "amount of power"
124  Sec. 3.7.4, lines 2,3   "X"-->"A", "Y"-->"B"
127  Line 2           "wierd" --> "weird"
128  Line 5           "to put information" --> "to put information on"
131  Line 9           "trransmission" --> "transmission"
131  Fig. 3-58        "VCC" --> "VDD"
132  Line 11          delete extra "a"
136  Line 9           "freqency" --> "frequency"
140  Table 3-8, line 3  "suppy" --> "supply"
140  Line 1           "FCT-T almost" --> "FCT-T are almost"
152  Line 9           "familes" --> "families"
153  Line 5           "than 0.2-V drop" --> "than the 0.2-V drop"
156  Line 6           "insufficent" --> "insufficient"
157  Line 4           "Q3 and Q4" --> "Q4 and Q5"
163  Line 15          "This more that an" --> "This is more than an"
164  Line 4           "pull-up" --> "pull-down"
     Lines 5-6        "more 0.5 V" --> "more than 0.5 V"
     Line -4          "more 2.3 V" --> "more than 2.3 V"
     Line -6          "inputs" --> "input"
165  Line -1          "is identical that" --> "is identical to that"
175  Line 5           "techologies" --> "technologies"
183  Fig. X3.17       "VCC" --> "VDD" (twice)
183  Fig. X3.17(b)    The topmost of two "Q5" transistors should be "Q3"
184  Drill 3.24(h)    "75 ohm" --> "270 ohm"
185  Drill 3.35       "footnote 6" --> "footnote 5"
185  Drill 3.38       This was intended to be two separate problems.
185  Drill 3.41       "typing" --> "tying"
201  Fig. 4-3(d)      Colors reversed in expression
202  Box line 1       "introduced Section" --> "introduced in Section"
262  Ex. 4.10(f)      Missing multiplication dots in expression
263  Ex. 4.15         "Sigma sub(W,X,Y,X)" --> "Sigma sub(W,X,Y,Z)"
263  Ex. 4.21(a)      Missing multiplication dot in expression
264  Ex. 4.25,4.26    "T11 apostrophe" --> "T11 prime"
322  Fig. 5-47        U5 should be 74x00, not 74x20 (two places)
322  Fig. 5-47        Signal RA2 output pin number "4" --> "6"
301  Box 1, line 2    "somwhat" --> "somewhat"
330  Fig. 5-53        Add reference designator U6 to NAND gate
335  Line 6           "multiplexer." --> "multiplexer in Figure 5-58(b)."
337  Line -2          "requires" --> "require"
346  Fig. 5-73 cap.   "8-bit" --> "4-bit"
349  Fig. 5-77        74x682 pin numbers "6"-->"19", "5"-->"1"
364  Fig. 5-89        Missing reference designators U1 and U2
381  Ex. 5.35, line 4  "three" --> "two or three"
387  Ex. 5.67, line 2  "three" --> "two"
401  Line 12          "predjudice" --> "prejudice"
417  Box line 1       "performer" --> "perform"
435  Line -10         "flip-flip" --> "flip-flop"
443  Ex. 6.15, line 3  delete "our"
456  Line 5           "mimimum" --> "minimum"
464  Figure 7-24      Second "Ignored since C is 0" should refer to K, not J
466  Lines -1,-3,-4   "E" --> "EN"
474  Fig. 7-36        "MAX" --> "MAXS" (four places)
474  Box caption      "Timing Diagrams" --> "Suggestive Drawings"
     Box line 2       "show" --> "shows"
483  Box line 6       "mS" --> "ms"
531  Lines 1,2        "EN" --> "X"
531  Lines 6,7        "X" --> "EN"
549  Line 7           Insert sentence: "(The 74x164 contains 8 flip-flops
                        in series as in the top half of Figure 8-7.)"
     Fig. 8-8         7430 output pin number "12" --> "8"
559  Line 18          "inputs on X and Y, since" --> "inputs on X and Y since"
561  Box line -1      "state-machine" --> "state machine"
566  Sec. 8.5.4, line 1   "for for" --> "for"
575  Fig. 8-24        This machine does not signal an error if a wrong button
                        is pushed at the same time as the correct one.
577  Ex. 8.4          "Exercise 8.13" --> "Table 7-18"
587  Fig. 9-3         Last digit of "74x112" is clipped in two places
657  Ex. 9.46         "Exercise 9.45" --> "Drill 9.8"
661  Fig. 10-1        Output pin O8 should be labelled "(12)" not "(13)"
670  Line 5           "A feedback input" --> "A feedback input is"
669  Box line 3       "slight" --> "slightly"
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679	Box line 1	"in the absence`" --> "in the absence of"
681	footnote	"Throughtout" --> "Throughout"
691	Fig. 10-17	"PAL16V8R" --> "GAL16V8R"
723	Line 3	"a bits" --> "bits"
724	Line 7	"plating" --> "playing"
	Line 8	"examples types" --> "examples"
746	Box line -8	"would to clock" --> "would clock"
	Box line -7	"location you" --> "location, you"
770	Line -5	"usual" --> "usually"
774	Box line -2	"does" --> "does not"
775	Line 15	"Figure 12-1" --> "Figure 12-2"
781	Line -1	"comapny" --> "company"
816	Index	clock skew Delete extra comma
832	Index	pull-down resistor Delete first comma
838	Index	transceiver 315 (in bold), 433